



GK-US035131

JPW
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re application of: | : | Attn: BOX ISSUE FEE |
| Seyeob KIM et al. | : | Patent Art Unit: 2817 |
| Serial No.: 10/673,555 | : | Examiner: Henry Choe |
| Filed: September 30, 2003 | : | Allowed: December 20, 2004 |
| For: TRANSDUCTANCE VARYING | : | |
| CIRCUIT OF TRANSDUCTOR | : | |
| CIRCUIT, VARYING BANDWIDTH | : | |
| FILTER CIRCUIT USING THE SAME | : | |
| AND DIGITAL TUNING CIRCUIT OF | : | |
| TRANSDUCTOR-CAPACITOR | : | |
| FILTER | : | |

SUBMISSION OF FORMAL DRAWINGS

Assistant Commissioner of Patents
Washington, DC 20231

Sir:

In accordance with the December 20, 2004 Notice of Allowance, Applicants submit herewith six sheets of formal drawings for the subject application, including Figures 1-6. The corrected formal drawings include the change required by the Notice of Allowance, specifically, Figure 1 now includes a legend that read -- Prior Art --. Entrance of these formal drawings as well as prompt publication of this application is respectfully requested.

If there are any questions regarding the proposed drawing corrections, please feel free to contact the undersigned.

Respectfully submitted,

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